



S H R I G O P A L

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OBJECTIVE

To work for an ambitious organization in a challenging and growing environment to make a competing society value adding tutor or lecturer for the students and delight them with scope of innovation, ingenuity, fast career development and ample opportunities to learn and nurture the subject

INSTITUTION “WORKING SINCE 2ND MAR 2010 TO 1ST MAR2013”

Powai Labs Technology Pvt. Ltd.
R&D Centre Registered Office
4th Floor, KReSIT, IIT Bombay, Powai, Mumbai

POWAILABS TECHNOLOGIES PVT. LTD. – AN I.I.T BOMBAY INCUBATED COMPANY
PowaiLabs is a pioneer and creator of IMAGE – Simulation accelerator, a high quality affordable and robust tool utilized to test and verify ASIC/FPGA Designs

Faculty Trainer: for the state of J&K, HP, Mumbai all technical institute for the VLSI

TEACHING AND TECHNICAL ABILITIES SINCE MAR 2014 TILL TODAY

Coaching Institutes Rathie Classes
IES/GATE , Delhi
Vidya Alankar Mumbai

Student doubt classes for GATE
and ESE of all subject excluding
microwave and computer
organization associated With
Rathie Sir

Lecture Completed
EDC/COMM/AE(including
digital)

Lecture at NIT Srinagar, Kashmir
University, Jammu University,
DRDO,

MLL	8085,8051 and processor architecture
Programming Language	C ,C#
HVL	System Verilog, UVM
HDL's	VHDL, Verilog
Simulators	Modelsim,GHDL, Iverilog.
Synthesizers	Xilinx ISE, Image
Some Other EDA Tools	Multisim, OrCad, CadStar, ChipScope,MatLab
OS	Linux ,Windows,Ubuntu,Fedora
HW Resource	FPGA, CPLD, PLX,EEPROM,SRAM etc

EXAMINATION QUALIFIED

GATE six times Since 2011,2012,2013,2014,2015,2016
Prashar Bharti
RRB Mumbai SSE
IES written
Nilet

TRAININGS UNDERGONE AND PROJECTS UNDERTAKEN

1. Powai Labs Technology Pvt. Ltd.

a) Assembly b) Testing c) Validation

2. ITI Ltd. Mankapur Gonda U.P.

Component Divisions,Circuit Divisions, SEA (switching exchange assembly) Divisions, New Products Divisions,G.S.M., B.T.S.

The above products are telecom products. Besides these I.T.I. also produces non telecom products like NCM (note counting machine), FNDM (Fake Note Detection Machine), Fire-alarms, Inverters etc.

PROJECT ON CLIENT SITE

1. Image VLSI Lab Installation and faculty training and Trainings for all polytechnic faculties at Sunder Nagar (A project of 1.00 Cr.)
2. Image VLSI Lab Installation and faculty training and at University of Jammu (A project of 2.49 Cr.)
3. Image VLSI Lab Installation and faculty training and at University of Kashmir (A project of 2.49 Cr.)

AWARDS

- Scholarship and Award for 2010 Powai Labs Technology "IIT Bombay"
- First price and award In 6th National Youth Parliament Competition in NVS

OTHER RELEVANT PERSONAL INFORMATION

Passport No. : H89576772
Languages Known : English, Hindi, Bengali

PLACE: Delhi
DATE : 1Feb 2017

(Shri Gopal)