

Curriculum vitae



ANKIT TRIVEDI

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Career Objectives:

To join a renowned institute that would help me in enhancing my skills and utilizing my technical knowledge to the maximum for the progress of institute and gaining expertise in professionalism.

EDUCATIONAL QUALIFICATIONS

Degree/ Education Level	Course Name	College/School	University/Board	Start & End Date	Specialization	Marks (%)
X Grade	High school	SV.M Inter college, kanpur(U.P.)	U.P. BOARD	1 st july 2006 to 10th June 2007	Science, Math, English	80%
XII Grade	Inter	SV.M Inter college, kanpur(U.P.)	U.P.BOARD	1 st July 2008 to 12 th june 2009	PCM	74.20%
Graduation	B. Tech.	R.K.G.I.T, Ghaziabad ,u.p	U.P.T.U.	10 th August 2010 to 28 th june 2014	Electronic and communication Engineering	75.92%
Higher Degree	M.Tech	Sant Longowal Institute Of Engineering And Technology, Sangrur(Punjab)	Deemed University (central govt. funded)	From-23 July 2015	VLSI	8.08(CGPA)

Type	Company	Start &End Month/year	Topic
Summer Training	HPCL,Kanpur	June 2012 to July2012.	EMBEDEED AND ROBOTICS
Summer Training	DOORDARSHAN KENDRA ,Lucknow	June 2013 to July2013.	BROADCASTING OF DOORDARSHAN

Technical skills

- VHDL Coding, Xilinx vivado software, FPGA board as zynq board , kintex kc705 board
- Electronics core subject
- implementation of interfaces such as SSI, DPRAM, Handling Analog/Discrete signals Serial (RS 422), Ethernet, CAN bus
- VLSI, Embedded technology and robotics
- M.S. office/Windows xp,98,7

Project and Seminar

- Worked on project "**IEEE 754 Single precision floating point arithmetic using Vedic mathematics using VHDL in FPGA**".
- Presented Seminar on "**E-WASTE system management**" in B-tech.
- Worked project on "**bank security system with auto dial up with RF communication using relay**".

List of Publication

1-ANKIT TRIVEDI, Single Precision Floating Point Arithmetic Using Vedic mathematics', ISSN: 2393-9028 (PRINT) |ISSN: 2348-2281 IJRECE VOL. 6 ISSUE 4 (OCTOBER- DECEMBER 2018)

2-ANKIT TRIVEDI, "Vedic Mathematics for High Speed Multiplier Designs A Review" in International Journal of Electronics, Electrical and Computational System (IJECS) ISSN 2348-117X Volume 6, Issue 6 June 2017.

3-ANKIT TRIVEDI, IEEE 754 Single Precision Floating Point Arithmetic Unit Using VHDL, www.jetir.org (ISSN-2349-5162)© 2019 JETIR April 2019, Volume 6, Issue 4

4-ankit trivedi, Single Precision Floating Point Arithmetic Using Vhdl Coding, International Research Journal of Engineering and Technology (IRJET) e-ISSN: 2395-0056

5-ankit trivedi, A Review on Single Precision Floating Point Arithmetic Unit of 32 bit Number, International Research Journal of Engineering and Technology (IRJET) e-ISSN: 2395-0056

6- ANKIT TRIVEDI, "Health Monitoring System using IoT using Arduino Uno Microcontroller", International Research Journal of Engineering and Technology (IRJET) e-ISSN: 2395-0056, Page no- 739-744, vol-6

Achievements

- Working as **Junior Research fellow** in **Indian Institute of Technology, Kanpur.**
- **GATE** qualified in the year **2017 with 2400 AIR RANK with 611 score.**
- **GATE** QUALIFIED in the year 2014, 2015, 2016, 2017, 2018 and 2019.
- Working as **GATE** and **IES** faculty in **Engineer career group**, Chandigarh.
- Working as **GATE** and **IES** faculty in **CATALYST Group**, Rajasthan.
- Working as Competition faculty in **Knowledge Zone**, Itanagar.
- Working as Competition faculty in **Engineers Headway**, Rajasthan.
- Working as Competition faculty in **Paramount**, Delhi.
- Working as Competition faculty in **KD campus**.
- Working as Competition faculty in **Skylight**, Delhi.
- Working as engineering completion faculty in **Reform Learning Center**, Gorkhapur.
- Worked as **Assistant Professor** in **G.C.R.G group of institution**, Lucknow.
- Worked as **Assistant Professor** in **Axis Engineering Colleges**, Kanpur.
- Faculty Development program in Analog circuit design, virtual lab and value education program.
- FDP on human values and professional ethics in BBD Lucknow.
- FDP on Analog Circuit in Texas Instrument.
- FDP on Virtual Lab in IIT Kanpur.

Subject Taught

- 1- Digital Electronic
- 2- Analog Electronic
- 3- Signal and System
- 4- Network Theory and Circuit
- 5- Analog and Digital Communication
- 6- Electromagnetic Field Theory
- 7- Control System

EXTRA-CURRICULAR ACTIVITIES

- Winner in **Movie Making competition** (Old Age Home).
- I have written a book name "A **LADY WITH NO TEARS**", published in Omji Publishing House Pvt. Ltd (2015)

PERSONAL DETAILS:

Name : Ankit Trivedi
Father's Name : Brijesh Trivedi
Date of Birth : 4th July 1992
Sex : Male.
Nationality : Indian.
Hobbies : Sketching, painting, poetry, story writing
Languages Known : English, Hindi
Address : Anand nagar shuklaganj unnao (u.p)

DECLARATION:

I hereby declare that the information furnished above is true to the best of my knowledge.

Ankit Trivedi

