SUTIRTHO BORAL

Electronics & Communication Engineer
BL-5, Flat no-301, Kendriya Vihar Phase-II,Kolkata-700051
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Summary

I am a keen enthusiast in electronics having a decent academic background starting from high school till Masters. I am very much interested in sharing my part of knowledge in electronics and also to gain experience regarding the same.

Education

Qualification	Institution	University/Board	Years	Stream	%,cgpa
Post-Graduate	Motilal Nehru National Institute of Technology, Allahabad	Motilal Nehru National Institute of Technology, Allahabad	2015 - 17	M.tech (Microelectronics and VLSI Design)	8.9
Undergraduate	Birla Institute of Technology	Birla Institute of Technology, Mesra	2010 - 14	B.E (Electronics and Communication)	7.33
Intermediate	Ahlcon Public School	CBSE	2009-2010	Science	85.2
High School	Ahlcon Public School	CBSE	2007-2008	-	89.8

Currently working as Junior Research Fellow(JRF) in DRDO (Lastec ,Delhi)

Software Skills

Software	Proficiency level
Tanner tool	Intermediate
Xilinx	Intermediate
Cadence	Intermediate

HARDWARE WORKED ON

- Rf/DC Sputtering machine (during Masters)

COMMUNICATED PAPER

- "Modeling and Analysis of Sputtering Process Parameter Dependence on Optical and Structural Properties of ZnO thin film" in Journal Of Applied Physics.

LANGUAGES KNOWN

- Fluent in English and Hindi
- Good Communication Skills

ACADEMIC PROJECTS

1. Implementation of Booth Multiplier in Verilog and interfacing with LCD in FPGA circuit.

Booth multiplier reduces the time to implement the multiplication and the output was being displayed on the lcd which was interfaced with the fpga kit.

2. Simulation of Home Security System using LabView.

In this project IR sensor were used . The alarm is controlled by the labview software that takes the input from the data acquisition system (DAQ). Lab view can be used to control a number of parameters. In accordance with the data taken from DAQ the output is controlled like switching on the alarm or sending email or sms alert. This project can be modified to a smart home with a number of sensors to automatically control outputs.

3. Design and simulation of phase locked loop using two different phase detector in L-T Spice

Using L-T Spice phase locked loop was simulated firstly using a phase comparator as phase detector and secondly using a XOR gate. The operating frequency and power drawn by both the circuits were compared.

4. Fabrication of ZnO thin film using Zn metal target by RF sputtering. (M.tech thesis)

ZnO thin film were fabricated on silicon substrate using RF sputtering method. Various sputtering parameters like pressure, time, power, target sputter distance were varied to fabricate thin film with different characteristics. Different characteristics of the thin film can be brought to use in suitable applications.

5. Reducing the problem of charge sharing in DOMINO CMOS LOGIC CIRCUITS.

Tanner Software was used to simulate the domino logic circuit. By adding PMOS transistors we can solve the problem of charge sharing.

ACHEIVEMENTS

1. Secured All India GATE Rank of 660 in year 2015. Marks (56.44)

2. "Chatra Pratibha Puraskar" from Hindi Sahitya Academy in High School

TRAININGS

1. Bharat Sanchar Nigam Limited

Training given by bsnl at advanced level telecom centre in Ghaziabad during May-June 2012

2. Programmable Logic Controller (PLC)

Have undergone 4 week course on PLC from MEMS Kolkata

Hobbies and interests
Playing table tennis and soccer Listening to soothing music Have interest in travelling. Interest in sketching.

PERSONAL DETAILS

Father's Name :	Supriyo Boral
Mother's Name :	Shikha Boral
Date of birth:	15th June 1992
Languages Known:	English, Hindi, Bengali (Fluent in all)
Permanent Address:	BL-5, Flat no-301,Kendriya Vihar Phase-II,Kolkata-700051

DECLERATION

I hereby declare that all the details given above are true to best of my knowledge and there is no discrepancy in it

Place: Kolkata

Sutirtho Boral

Date: 04/07/2017