



# MUKUL KANT

Former Post Graduate Intern

Birla Institute of Technology & Science, Pilani

## PROFILE

Former Post Graduate Intern (Jan, 2023 – Jun, 2023) at the WILP department of BITS Pilani Hyderabad Campus and a TA (Aug, 2021 – Dec, 2022) at BITS Pilani, Pilani Campus. Experienced Senior Systems Engineer with a demonstrated capability of working in the information technology industry. A keen learner, team player and a maker at heart.

## ACHEIVEMENTS

- GATE 2020 (EC) Qualified
- Joint Convener, Pratigya Society, Thapar University (Jan, 2013 – Dec, 2014)
- NCC 'A' Certificate holder (2008)

## CONTACT

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WEBSITE:

- <https://github.com/mukulcant-mikemighty>
- <https://www.linkedin.com/in/mukulcant-0000564>

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## PROJECTS

- Report on UAV Communications via IRS - Advanced Digital Communication
- Analysis of Uplink IRS-Assisted NOMA under Nakagami - M Fading via Moments Matching - Wireless Communication
- Blind Source Separation of Speech Signals using Kurtosis, Negentropy and Maximum Likelihood - Advanced Digital Signal Processing
- Designing a 4-bit Binary Synchronous Counter (using D Flip Flop) using two phase dynamic flip-flop circuits working up to 2 -GHz with PVT variation of 10%. - VLSI Design
- Remote Controlled Spy robot with night vision camera

## EDUCATION

### Birla Institute of Technology & Science, Pilani (Pilani Campus)

2021 - 2023

M.E. in Communications Engineering

C.G.P.A.: **6.94**

### Thapar University, Patiala

2012 - 2016

B.E. in Electronics Instrumentation & Control

C.G.P.A.: **7.16**

## WORK EXPERIENCE

### WILP BITS Pilani (Hyderabad Campus) [Post Graduate Intern]

Jan, 2023 – Jun, 2023

- Developed knowledge on concepts like vehicular communication, **AUTOSAR**, V2X etc.
- Worked on an **IoT** project to automate the warehouses under the concept of Industry 4.0 which utilized my expertise in embedded systems, C Programming, RTOS, etc.
- Also, assisted in problem faced by my mentor pertaining to control actuation using **Real time video processing** (gave them a multi-threaded approach as the solution).

### Infosys Ltd. [Senior Systems Engineer]

Apr, 2019 – Feb, 2021

Worked as **full stack web app developer** (JAVA backend and Angular frontend) in their **emerging technologies unit**: iCETS

### Infosys Ltd. [Systems Engineer]

Oct, 2017 – Apr, 2019

Worked in their **data and analytics unit**: DNA

### Infosys Ltd. [Systems Engineer Trainee]

Jan, 2017 – Oct, 2017

Trained on various concepts and technologies such as: Python, OOP, DSA, DBMS, SQL, JAVA, Angular etc.

### I.N.M.A.S. (D.R.D.O.) [Graduate Intern]

Jan, 2016 – Jun, 2016

Interned in their Biomedical department as an Instrumentation Engineering Intern. Worked on **Biomedical DSP applications** on various tools like NI LabVIEW, MATLAB etc.

## SKILLS

**Programming Languages:** C, Python, JAVA, Typescript, MATLAB

**Operating Systems:** Linux, Windows

**Frameworks:** Angular, Hibernate, Spring

**Databases:** MS SQL Server, MySQL

**CI/CD:** Git, GitLab

**Graphical Programming Environments and Simulators:** Simulink, NI LabVIEW, LtSPICE, NI Multisim

**Microcontroller Experience:** 8051, Arduino, ESP32, PIC-16, TI MSP-430, TI MSP-432

**Concepts:** Data Structures, Algorithms, OOP, Digital Signal Processing, Digital Circuit Design, Analog Circuits, Computer Architecture, Embedded Systems, Advanced Digital Communication, Wireless Communication, Information and Coding Theory, VLSI Design