Curriculum Vitae

(Gunjan Gandhi)



Objectives:

To serve Institute with full motivation and dedication and make myself beneficial asset for the Institute.

Personal Information:

Name: Gunjan Gandhi

Date of Birth (dd/mm/Year): 07-03-1987 Present Address: Chhattarpur, New Delhi

Personal Contacts:

a) Mobile No: +91-9971895450 **b**) Email: gs.2507gunjan@gmail.com

Academic Background:

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Degree	Discipline/ Specialization	Percentage / CGPA	Passing Year	Institution/City
MTech	Telecommunication Technology	7.5	2012	IIT Delhi
BE	Electronics & Telecommunication	70.51	2009	BIT, Durg
HSSC	Maths	85.60	2005	Bharat Mata, Bilaspur
HSC	Science	83.83	2003	Bharat Mata, Bilaspur

Courses Preference/ Courses Taught (Offline and Online):

- Signals & Systems
- Network Theory
- Control System
- Communication System
- Digital Electronics
- Electromagnetic Field Theory

Content Developed:

- GATE Previous Years Solution
- GATE and IES Test Series
- JTO Previous Years Solution
- Subject Books Review and Corrections

IIT Delhi Thesis:

Title - Controlling The MEMS Mirrors Using ARM9 for Beam Pointing in a Free Space Optical Satellite Link **Supervisor -** Prof. Subrat Kar and Prof. V.K. Jain

Description - This Project is Sponsored by NPMASS ADA

The Objectives of this project are as follows:

- 1. To make a prototype to replace the bulky mechanical mirrors used in ground stations by small electronic Micro-Electro Mechanical System (MEMS) mirror array.
- 2. Control these MEMS mirror by FPGA using Software control and real time control system.

Projects:

- Automatic Room Light Controller With Digital Visitor Counter (July, 2008 May, 2009): The Objectives of this project are as follows:
 - 1. It controls the room lights deciding on whether any person is present in the room or not.
 - 2. It displays number of person present inside the room.
- Calculator using FPGA (Mar, 2011 Apr, 2011): The aim of this project is to design a Calculator using Virtex 2 Pro and EDK.FPGA can provide high computing speed by performing all the computation in one clock cycle. So this project is build to perform all the arithmetic and logical function at the higher speed.

Qualifying Exam:

GATE 2010 & GATE 2018

Work Experience:

- Lovely Professional University (July, 2012 June, 2015)
- Gate & IES Coaching Center (Offline and Online) (July, 2015 Present)

Positions of Responsibility:

- Course Coordinator, Lovely Professional University (July, 2013 present)
- Chief Examiner, Lovely Professional University (July, 2012 present)
- Placement Coordinator Lovely Professional University (July, 2014 present)
- State head for Chhattisgarh admission team LPU (2 years)
- Maintenance committee member, Aravali House, IIT Delhi (July, 2011 May 2012)

Scholastic Achievements:

- Training on Embedded AXI: Training on Embedded AXI conducted during Xilinx technical Summit 2011.
- **ABVP merit certificate**: Awarded the ABVP merit certificate for securing the 1st position in the 10th board
- **BMHSS merit certificate:** Awarded the BMHSS merit certificate for securing the 2nd position in the 11th.

Publication:

- Akash Saini and Gunjan Gandhi, "Fine Tuning Approach for Quality of Service in Border Gateway Protocol", IJAIR, Vol. 2, Issue 2, 2013
- Md Samil Farouqui; Vishal Nigam and Gunjan Gandhi, "Evolution of the Routing Protocols from RIP (1969) to OSPF (1998)", IJARCSSE, Volume 4, Issue 2, pp.750-754 February 2014.
- Vishal Nigam; Md Samil Farouqui and Gunjan Gandhi, "Enhanced Comparative Study of Networking Routing Protocols", IJARCSSE, Volume 4, Issue 2, pp.742-749, February 2014.
- Sumit Angrish and Gunjan Gandhi, "Enhancement in Path Diversity Technique in Inter Domain Routing Using Border Gateway Protocol", IJETT, Volume 11 No 1, pp.10-13, May 2014.
- Vishal Chhabra and Gunjan Gandhi, "A Survey on Routing Protocols in Wireless Sensor Network", IJAIR, Volume 3 Issue 10, pp.195-200, Nov 2014.
- Harmanjit Kaur and Gunjan Gandhi, "Performance Analysis of TARF for Wireless Sensor Network", IIJEC, Volume 3 Issue 4, pp.32-36, April 2015.